





# Daniel Y. Lee, Ph.D.

## Associate

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## Overview

### About Daniel

Daniel Lee is an associate\* in the Washington, D.C., office of Fish & Richardson P.C. His areas of technical expertise include integrated circuits (IC), fiber optics, silicon photonics, nanofabrication, MEMS, 3D semiconductor integration, control systems, data center networking, and data modulation formats.

As a registered patent attorney and former technology specialist at the firm, Dr. Lee has experience in all phases of patent prosecution, and has also worked on several *inter partes* review (IPR) proceedings. He has worked with clients ranging from early-stage startups to large multinational companies in developing their patent portfolios and prosecuting patents for their most important technical innovations.

Prior to joining the firm, Dr. Lee was a senior hardware engineer for a multinational computer technology corporation, where he developed photonic circuits and lasers for 100Gb+ datacom and managed optical transceivers used in data centers. He has industry experience in semiconductor layout, circuit design, board design, and testing. He is also the author of several peer-reviewed publications on silicon photonics, inventor on six issued patents, and a winner of the prestigious IBM Ph.D. fellowship. Dr. Lee is fluent in Korean.

*\*Not admitted to practice in Washington, D.C. Work conducted in Washington, D.C. is directly supervised by a member of the Washington, D.C. bar or limited to federal courts and agencies listed below.*

# Focus Areas

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## Services

- Litigation
- Patent
- Post-Grant

## Industries

- Consumer Products
- Hardware
- Internet
- Nanotechnology
- Optics
- Semiconductors
- Software
- Telecommunications

## Education

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J.D. *cum laude*, New York University School of Law (2021) Staff Editor, *Journal of Intellectual Property and Entertainment Law*, Florence Allen Scholar

Ph.D., Electrical and Computer Engineering, Cornell University (2015)

M.S., Electrical and Computer Engineering, Cornell University (2013)

B.S. *magna cum laude*, Electrical and Computer Engineering, Cornell University (2010)

## Insights

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## Publications

D. Y. Lee et al., "Error-free operation of a polarization-insensitive 4 x 25 Gbps silicon photonic WDM receiver with closed-loop thermal stabilization of Si microrings", *Optics Express* (2016).

Y. D. Lee, M.O. Thompson, M. Lipson, "Deposited low temperature silicon GHz modulator", *Optics Express* (2013).

## Recognition

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Recipient of the IBM Ph.D. Fellowship award (2014-2015)

## Languages

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- English
- Korean